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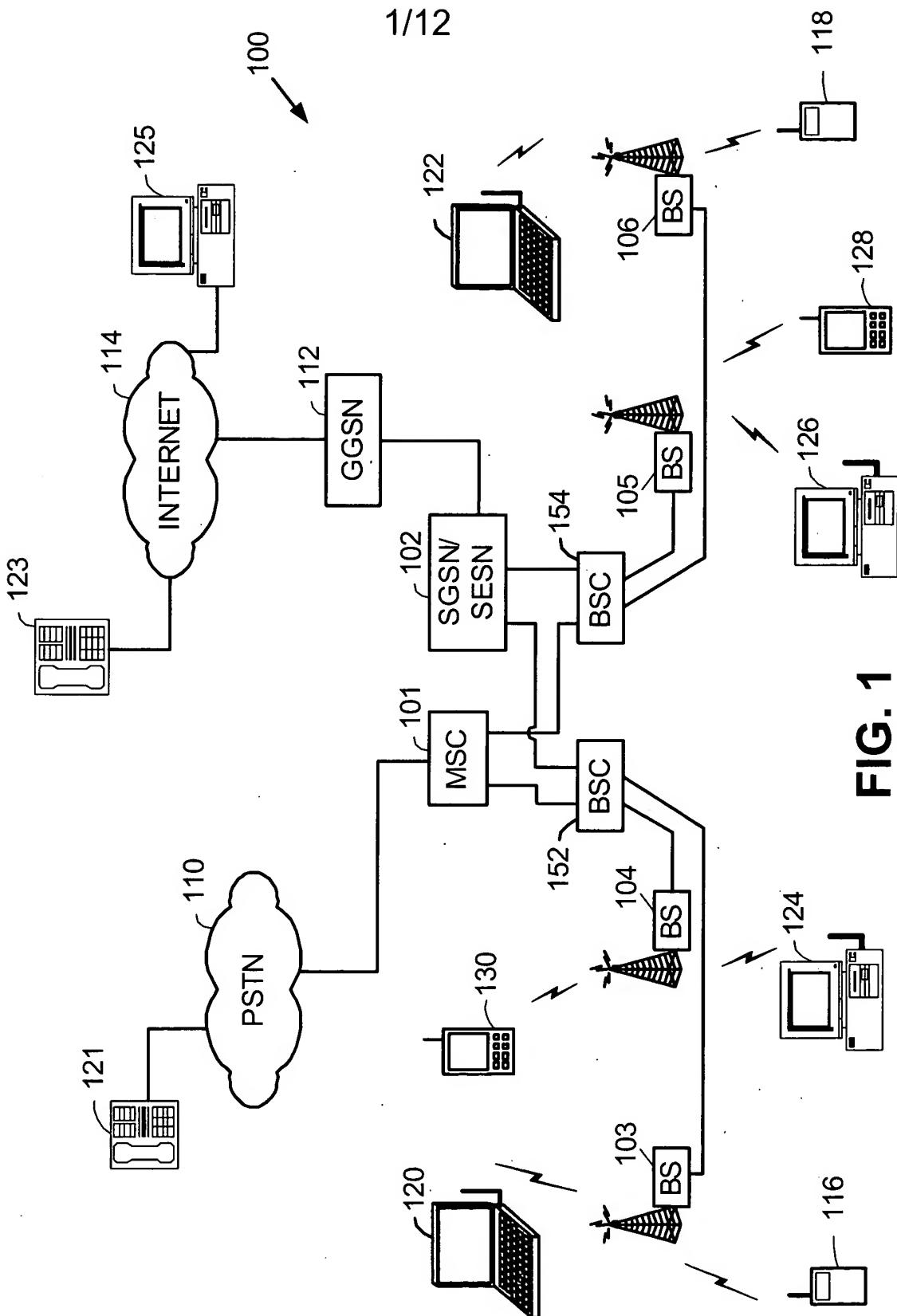


FIG. 1

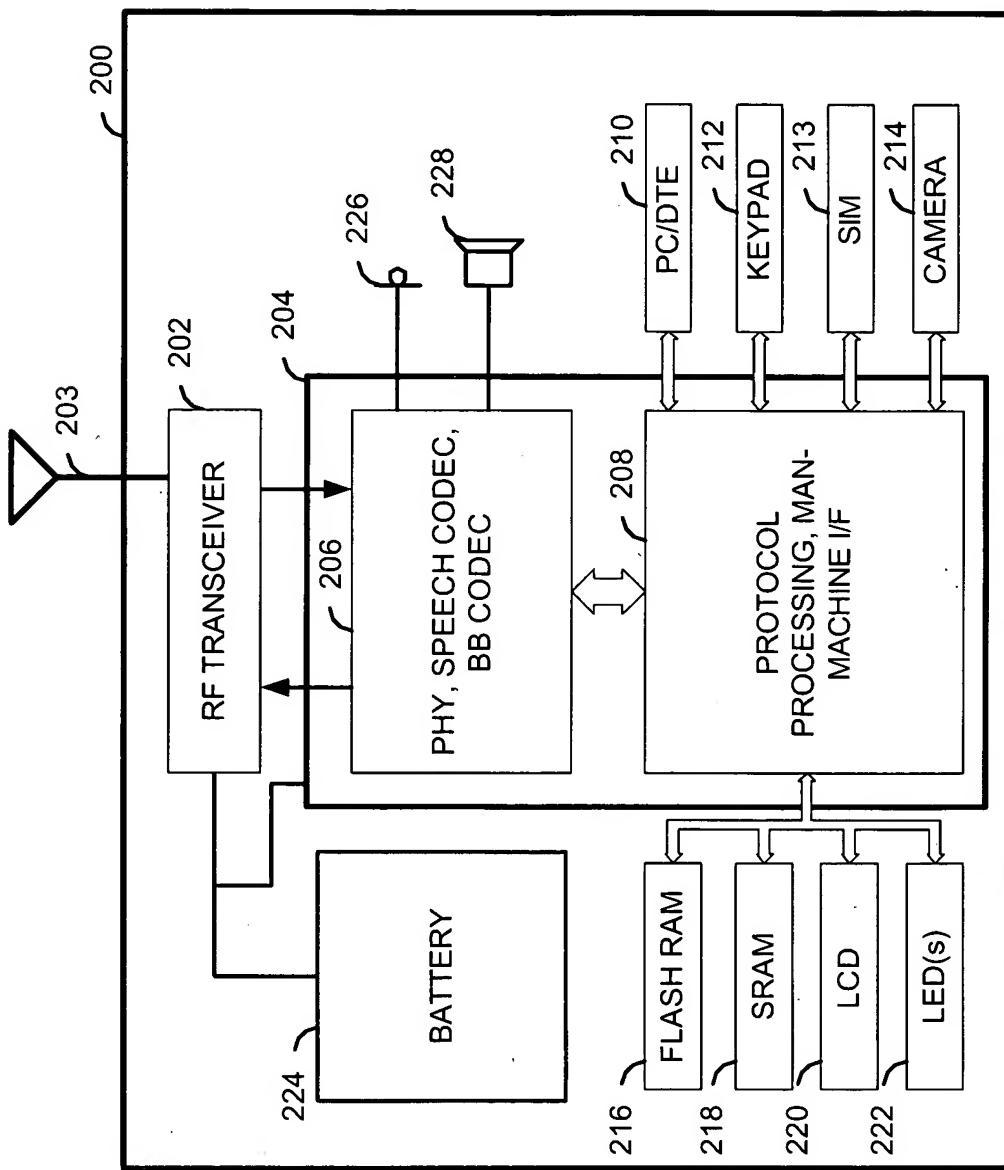
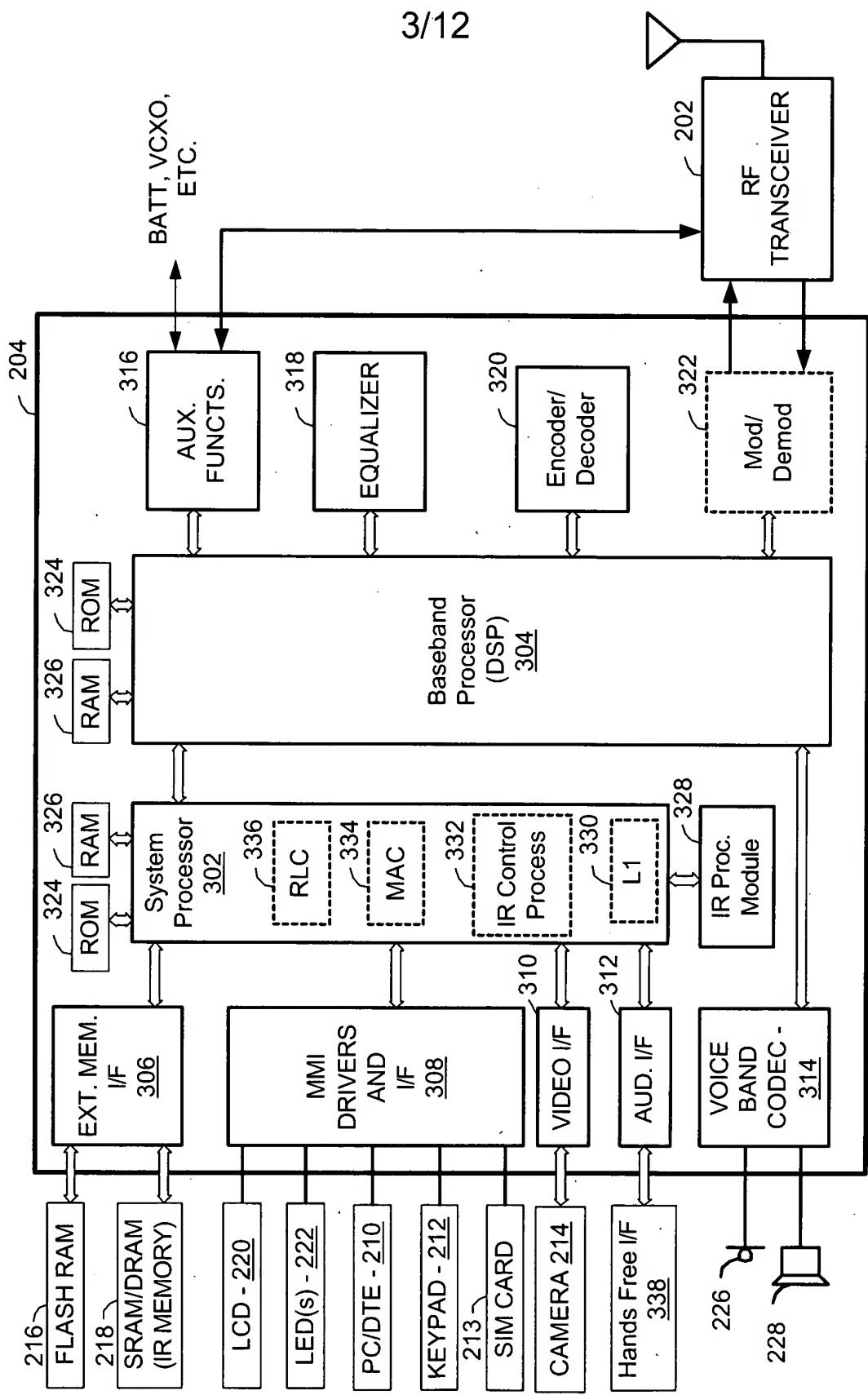
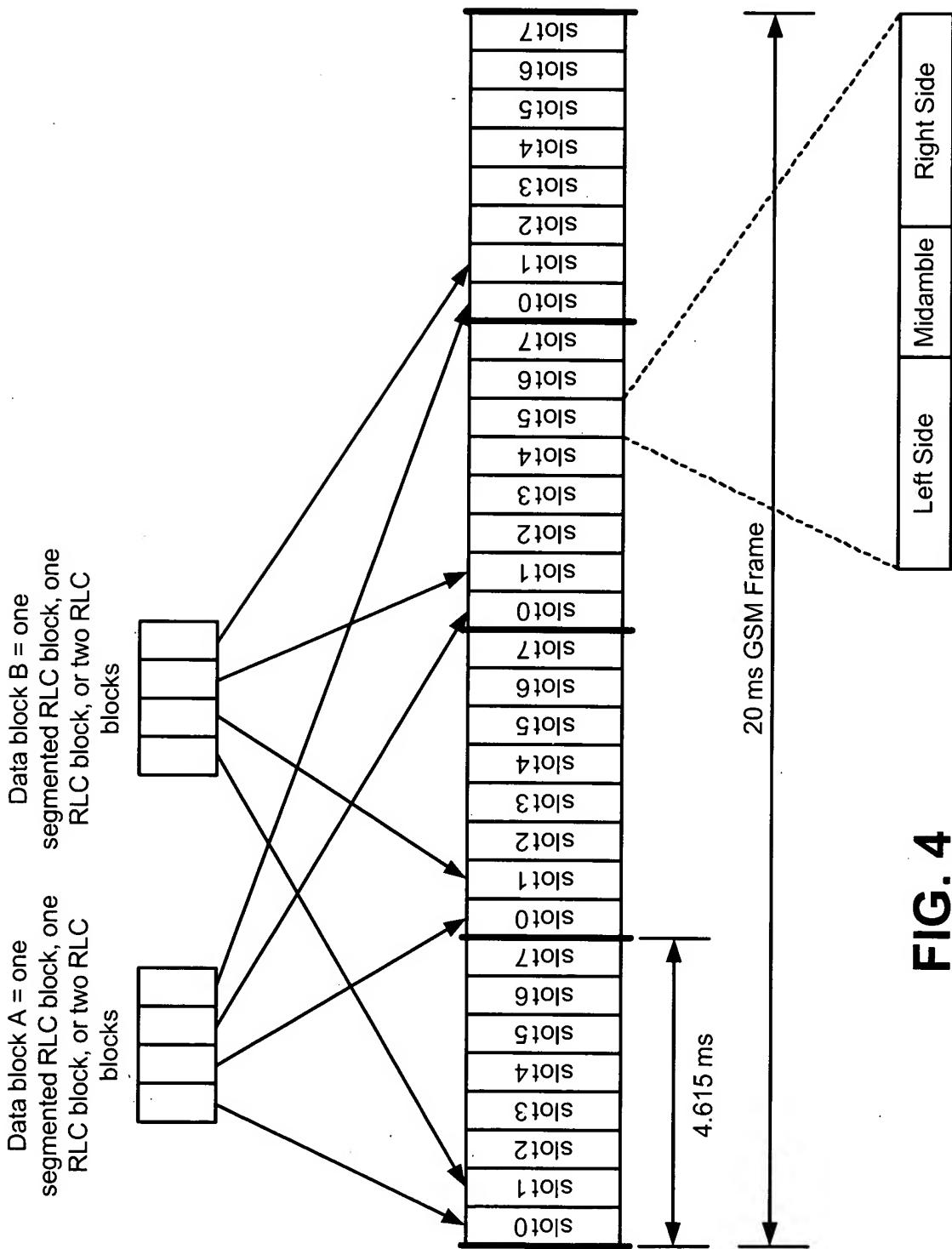
**FIG. 2**

FIG. 3



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**FIG. 4**

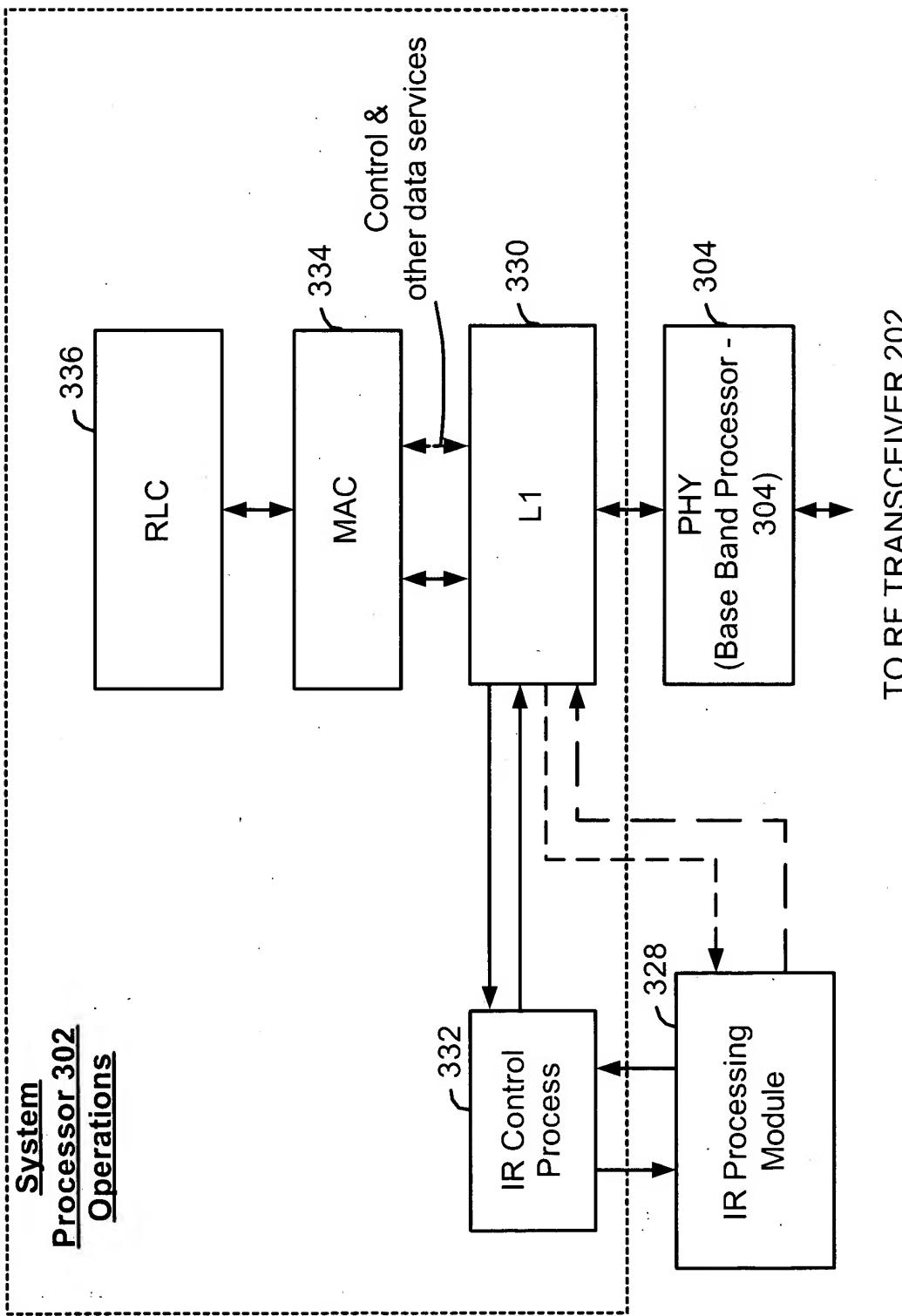


FIG. 5A

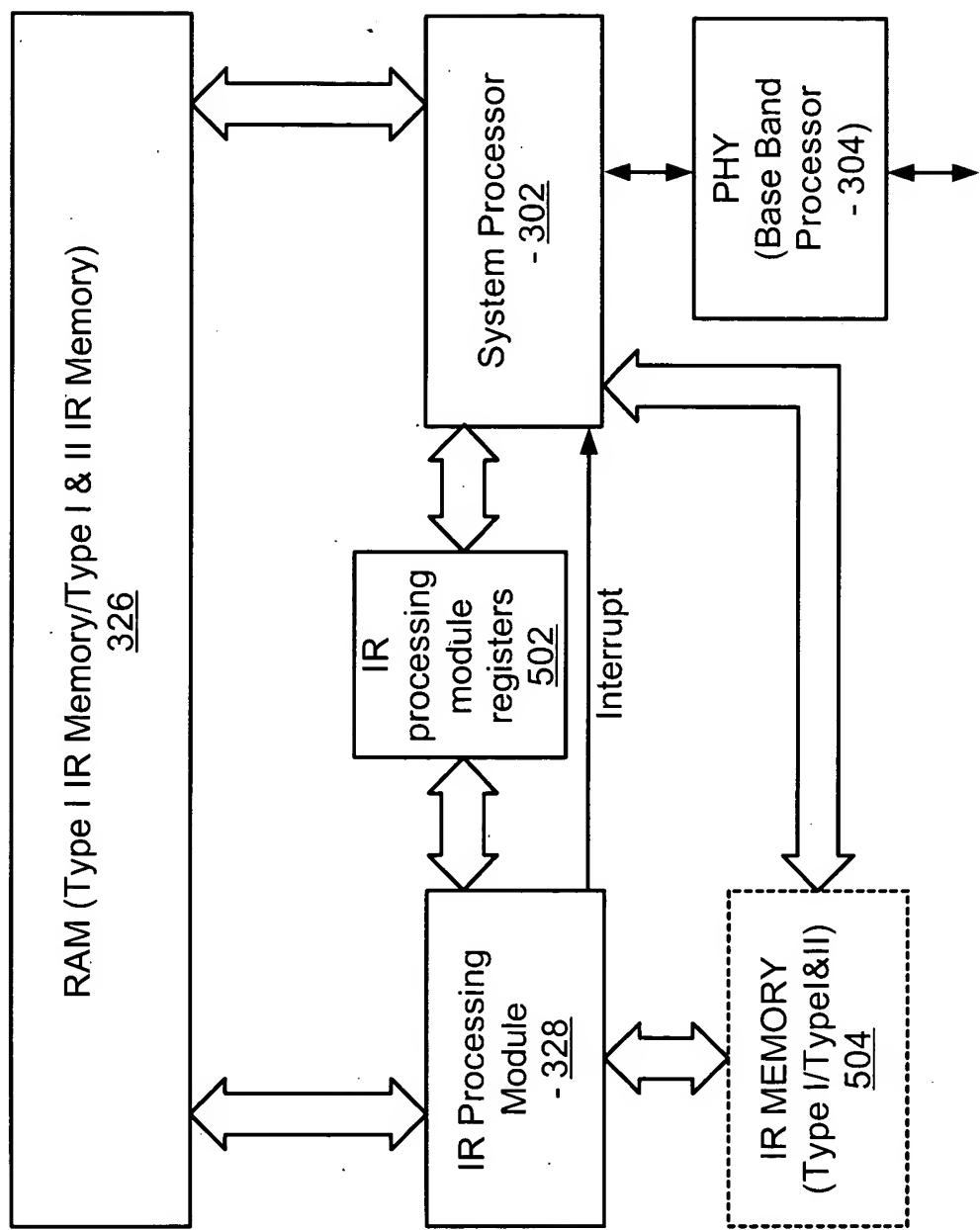
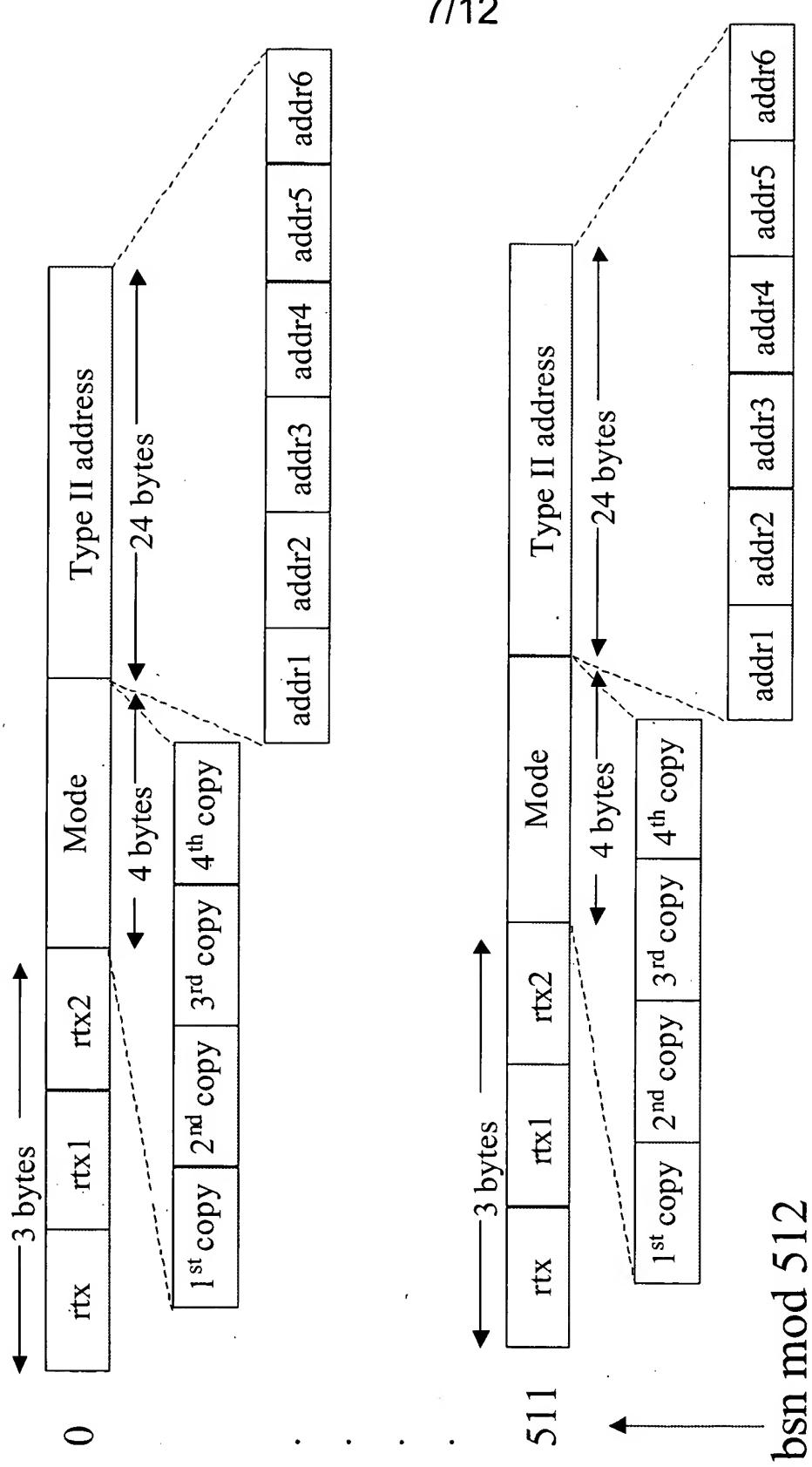


FIG. 5B

FIG. 6



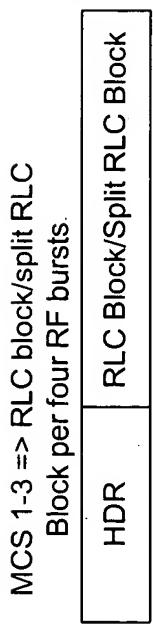


FIG. 7A

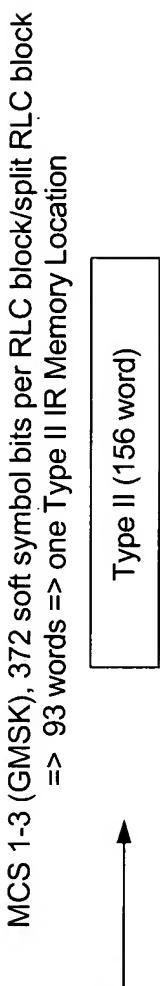


FIG. 7A

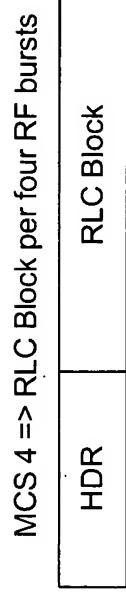


FIG. 7B

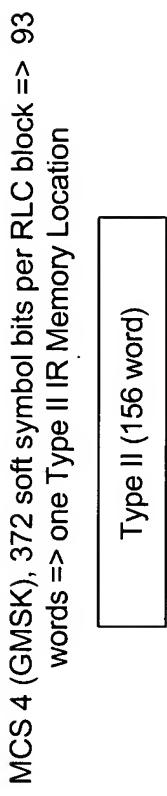


FIG. 7B

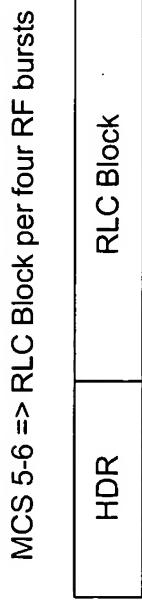


FIG. 7C

MCS 5-6 (8-PSK), 1248 soft symbol bits per RLC block
=> 312 words => two Type II IR Memory Locations

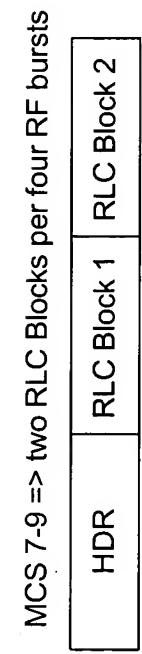
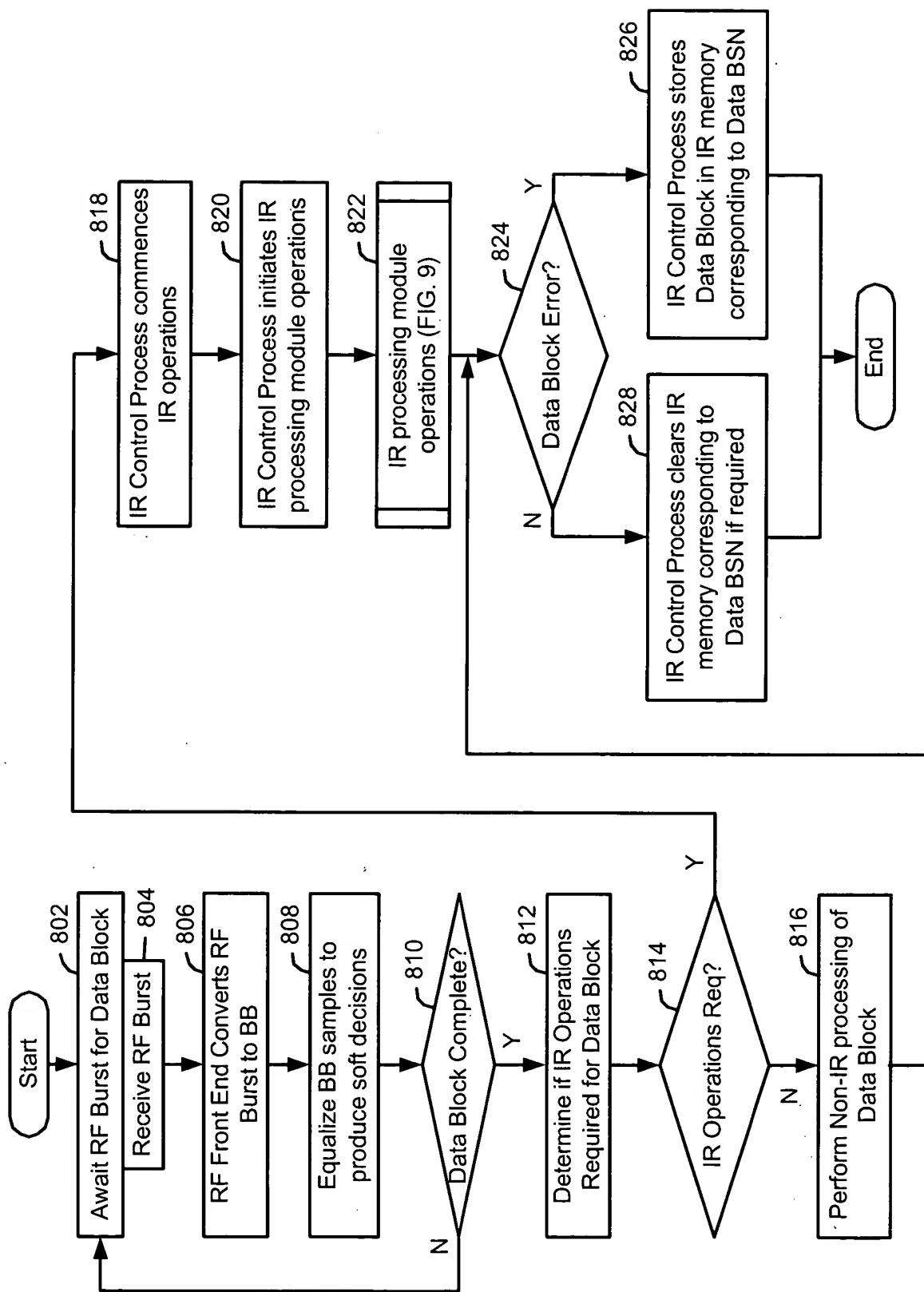
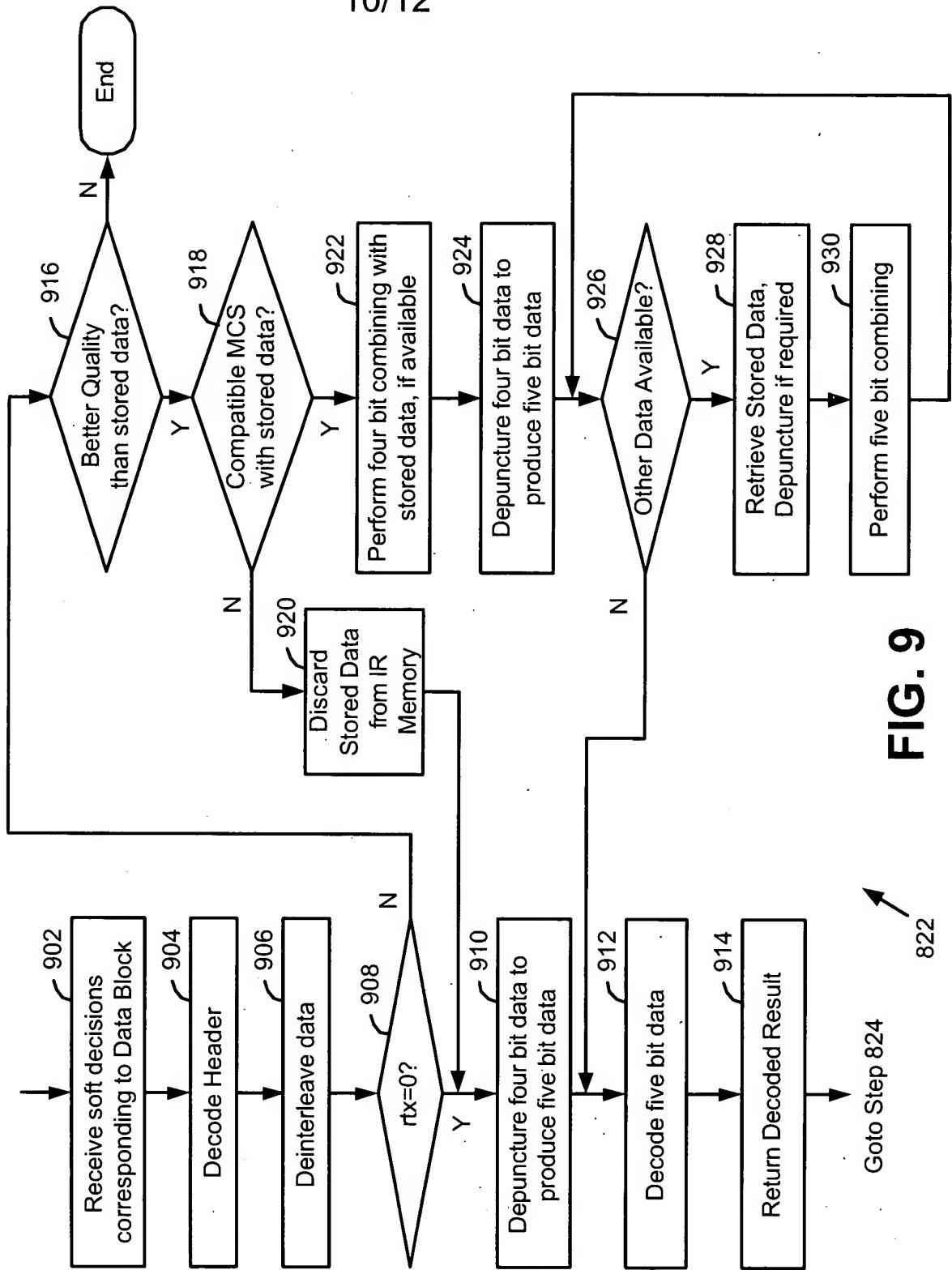


FIG. 7D

MCS 7-9 (8-PSK), 612 soft symbol bits per RLC block => 153 words => one Type II IR Memory Location



**FIG. 8**



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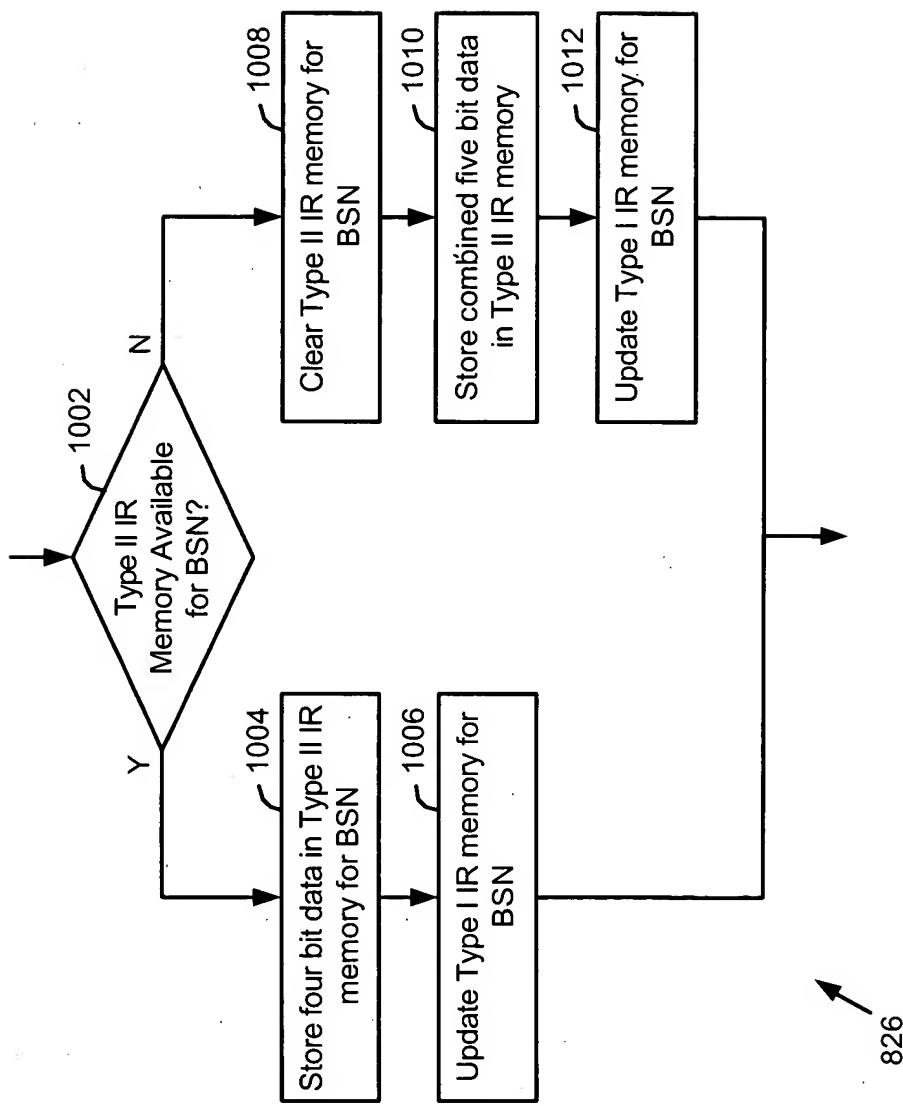


FIG. 10

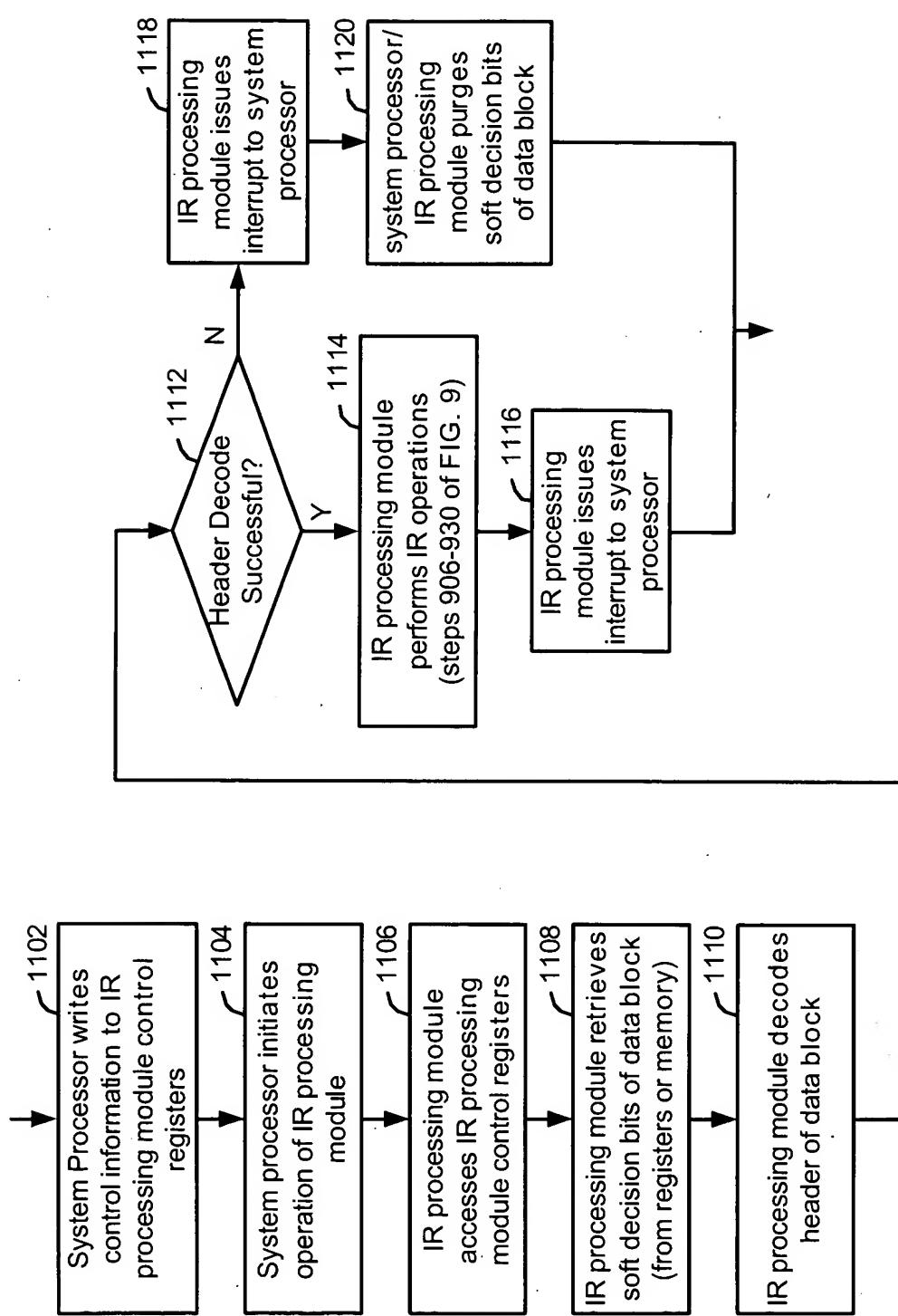


FIG. 11